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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Esposito et al.

Atty Dkt No.: ALTRP092/A1035

Application No.: 10/804,294

Examiner: UNASSIGNED

Filed: March 18, 2004

Group: UNASSIGNED

Title: EFFICIENT IMPLEMENTATION OF
MULTI-CHANNEL INTEGRATORS AND
DIFFERENTIATORS IN A
PROGRAMMABLE DEVICE

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as first-class mail on February 15, 2005 in an envelope addressed to the Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450.

Signed: _____

Joyce L. Ferreira
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**INFORMATION DISCLOSURE STATEMENT
37 CFR §§1.56 AND 1.97(b)**

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir: ,

The references listed in the attached PTO Form 1449, copies of which are attached, may be material to examination of the above-identified patent application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR §§1.56 and 1.97. The Examiner is requested to make these references of official record in this application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

This Information Disclosure Statement is: (i) filed within three (3) months of the filing date of the above-referenced application, (ii) believed to be filed before the mailing date of a first Office Action on the merits, or (iii) believed to be filed before the mailing of a first Office Action after the filing of a Request for Continued Examination under §1.114. Accordingly, it is believed that no fees are due in connection with the filing of this Information Disclosure Statement. However, if it is determined that any fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 500388 (Order No. ALTRP092).

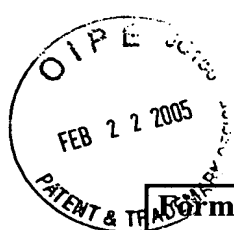
Respectfully submitted,

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Form 1449 (Modified) Information Disclosure Statement By Applicant (Use Several Sheets if Necessary)	Atty Docket No. ALTRP092/A1035	Application No.: 10/804,294
	Applicant: Esposito et al. Filing Date March 18, 2004	Group 2631

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A	6,401,230	06/04/02	Ahanessians et al.			02/01/99
	B	6,600,788	07/29/03	Dick, et al.			09/10/99
	C						
	D						
	E						

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	O	San et al., "Finite Impulse Response (Fir) Filter Compiler," U.S. Patent Application No. 09/773,853, filed January 31, 2001
	P	D'souza Et Al., "Dynamically Re-Configurable Signal Decimation System Using A Finite Impulse Response (Fir) Filter," U.S. Patent Application No. 10/396,194, filed March 24, 2003
	Q	Shung et al., "An integrated CAD system for Algorithm-Specific IC Design", IEEE Transactions on Computer-Aided Design, Vol. 10, No. 4, April 1991
	R	Saramaki et al., "Digital Filtering in FPGAs", 1994 Conference Record of the Twenty-eighth Asilomar Conference on Signals, Systems, and Computers, Vol. 2, pp. 1373-1377, 1994
	S	Jain et al., "FIRGEN: A computer-aided design System for High Performance FIR Filter Integrated Circuits", IEEE Transactions on Signal Processing, Vol. 39, No. 7, July 1991
	T	San et al., "Finite Impulse Response (Fir) Filter Compiler," U.S. Patent Application No. 09/773,853, filed January 31, 2001, Office Action Mailed July 15, 2004
	U	San et al., "Finite Impulse Response (Fir) Filter Compiler," U.S. Patent Application No. 09/773,853, filed January 31, 2001, Final Office Action Mailed December 17, 2004
Examiner		Date Considered

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.